

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	188	((714/710.ccls. or 714/711.ccls. or 714/718.ccls. or 365/185.05.ccls. or 365/200.ccls.) and @ad<"20011220") and (processor or (logic adj unit)) and (setting with (circuit or memory))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 13:59
L2	1	I1 and ((self with repair) and ((chain with scan) or (serial with test\$3)))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 13:36
L3	67	I1 and repair\$3	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:00
L4	66	I3 and memor\$3	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:00
L5	7	I4 and (set\$4 adj memor\$3)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:00
L6	67	(setting adj (memory or circuit)) and repair with memory	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:05
L7	30	I6 and @ad<"20001220"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:05
L8	493	"365/185.05".ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:20
L9	3	L8 and (fus\$3 with repair)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:20
L10	574734	(latch or register or (flip adj flop) or (scan adj chain) or LSSD or bsc or (scan adj cell))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21
L11	64693	L10 with switch	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21
L12	18034	L11 and memory.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21
L13	300	L12 and (setting adj memory)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21
L14	240	L13 and @ad<"20001220"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21

L15	143	L14 and (register with switch)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21
L16	43	L15 and (setting adj memory).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:21
L17	50	((microprocessor or (data adj process\$3)) and (fus\$1) and (setting adj memory))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L18	11	L17 and switch.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L19	6526	L12 and register.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L20	2783	L19 and switch.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L21	222	L20 and (switch with buffer).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L22	4	L21 and fus\$1.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L23	1539	L20 and (switch with register).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L24	22	L23 and fus\$1.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:22
L25	22	L23 and fus\$1.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 14:59
L26	307	kaiser-r\$.in. or schamberger-f\$.in.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 15:00
L27	37	I26 and process\$3.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 15:00
L28	1	I27 and buffer.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 15:02
L29	461	714/710.ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 15:03
L30	11	I29 and (buffer adj memory).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 15:03

L31	5	I30 and process\$3.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/12/05 15:03
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